

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A driving apparatus of a plasma display panel (PDP), comprising:

a digital data receiver for receiving a digital video data signal and a synchronous signal;

a digital video controller for supplying gamma-corrected digital video data signal to the digital data receiver and for supplying sustain pulse number information to the digital data receiver;

a multi-chip module in which a plurality of control chips having a control circuit for controlling the PDP, and a plurality of memories are mounted on a single package on a single printed circuit board (PCB) of a control board separately from the digital data receiver and the digital video controller;

a plurality of buffers for buffering signals between the multi-chip module and a plurality of driving units,

wherein the multi-chip module is mounted on a printed circuit board (PCB) of a control board, and

wherein the multi-chip module includes a plurality of green tapes, and input/output (I/O) lines coupling the plurality of control chips and the plurality of memories are formed in the plurality of green tapes within the single package.

2. (Currently Amended) The driving apparatus according to claim 1, wherein the single package is a ball grid type.

3. (Currently Amended) The driving apparatus according to claim 1, wherein the multi-chip module transmits a control signal ~~to each driving unit~~ via the PCB.

4. (Currently Amended) A ~~driving apparatus of a plasma display panel (PDP), display~~ device comprising:

a control board provided with a multi-chip module in which a plurality of control chips having a control circuit for controlling ~~[[the]]~~ a plasma display panel (PDP), and a plurality of memories are mounted on a single package on a single printed circuit board of the control board, the multi-chip module including a plurality of green tapes, and input/output (I/O) lines coupling the plurality of control chips and the plurality of memories being formed in the plurality of green tapes within the single package;

a plurality of driving units for generating and applying a driving signal corresponding to a control signal generated from the control board; and

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~~[[a]]the PDP for displaying an image by a plasma discharge according to the driving signal applied from each of the plurality of driving units, wherein the multi-chip module is mounted on a printed circuit board (PCB), and input/output (I/O) lines connecting the plurality of control chips and the plurality of memories are not formed directly on the PCB but are formed within the single package~~

wherein the control board comprises:

a digital data receiver for receiving a digital video data signal and a synchronous signal,

a digital video controller for supplying gamma-corrected digital video data signal to the digital data receiver and for supplying sustain pulse number information to the digital data receiver, and

a plurality of buffers for buffering signals between the multi-chip module and the plurality of driving units, and

wherein the multi-chip module is mounted on the single printed circuit board separately from the digital data receiver and the digital video controller.

5. (Currently Amended) The ~~driving apparatus display device~~ according to claim 4, wherein the single package is a ball grid type.

6. (Canceled)

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7. (Currently Amended) ~~The driving apparatus display device~~ according to claim 4, wherein at least one of the control chips is an ASIC type having a control circuit.

8-12. (Canceled)

13. (Currently Amended) A plasma display panel (PDP) driving apparatus comprising:  
~~a control board having a circuit board and a multi-chip module on the circuit board, the multi-chip module including a plurality of control chips and a plurality of memories on a single package, the control chip including a control circuit to control a PDP,~~  
a digital data receiver for receiving a digital video data signal and a synchronous signal;  
a digital video controller for supplying gamma-corrected digital video data signal to the digital data receiver and for supplying sustain pulse number information to the digital data receiver;  
a multi-chip module in which at least one of a plurality of control chips having a control circuit for controlling the PDP, and at least one memory of a plurality of memories are mounted on a single package on a single printed circuit board of a control board separately from the digital data receiver and the digital video controller; and  
a plurality of buffers for buffering signals between the multi-chip module and a plurality of driving units;

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wherein the multi-chip module includes a circuit package having a plurality of circuit layers, and

wherein at least one of the plurality of control chips and at least one of the plurality of memories are formed on a front of the circuit package and input/output (I/O) lines are formed through the plurality of circuit layers, and the I/O lines connect the at least one control chip and the at least one memory within the single package.

14. (Currently Amended) The PDP driving apparatus according to claim 13, wherein the single package comprises a ball grid type.

15. (Currently Amended) The PDP driving apparatus according to claim 13, wherein the multi-chip module transmits a control signal ~~to each driving unit~~ via the circuit board.

16. (Currently Amended) The PDP driving apparatus according to claim 13, ~~further comprising a~~ wherein the plurality of driving units to generate and apply driving signals corresponding to control signals received from the ~~control board~~ plurality of buffers.

17. (Currently Amended) The PDP driving apparatus according to claim 16, ~~further comprising~~ wherein the PDP to display an image by a plasma discharge based on the driving signals applied from each of the plurality of driving units.

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18. (Previously Presented) The PDP driving apparatus according to claim 13, wherein the control chip comprises an ASIC type.

19-22. (Canceled)